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In re application of:

Tao-Jung LEE, et al.

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Examiner:

Pham, Ly D.

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DUAL PORT SEMICONDUCTOR MEMORY DEVICE

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U.S. PATENT DOCUMENTS

Exam <u>Init</u>	Ref	Document Number	Issue Date	Name	Class	Sub Class
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LP		0530988 A2	Mar. 10, 93	Europe	Thomas J. Runaldue et al.

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Examiner: /Ly Pham/

Date Considered: 05/02/2006